



200MHz LOGIC ANALYZER

TE7100

- 200MHz sampling rate
- 16 Channels
- 128K samples/channel buffer
- Flexible trigger options
- Variable input threshold
- Variable pre-/post-trigger buffer
- External clock input
- Digital logger
- Connects to PC printer port, ISA
- PCI or PXI Bus



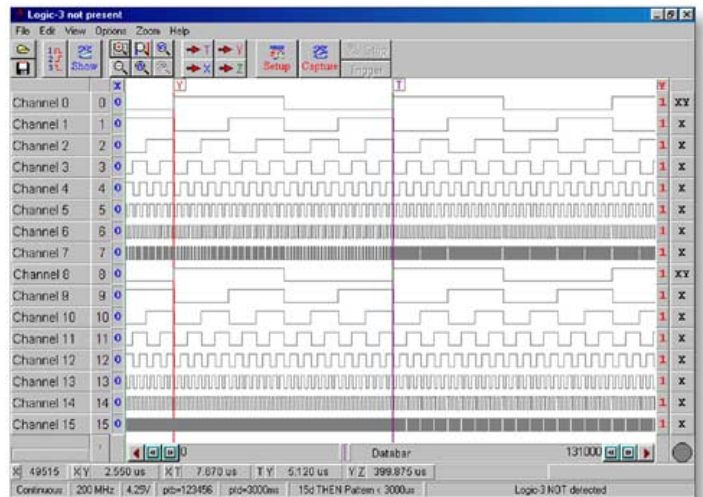
Capability To Go A Long Way

With the very flexible trigger capabilities of the TE7100 you can easily capture the exact data you want to view. Edge triggering may be set for a rising edge, falling edge or change-of-state. Pattern (level) conditions are set by specifying a "1", "0" or "don't care" on all channels. Combinations such as Edge OR Pattern, Edge AND Pattern, Edge THEN Edge, etc, are available. Triggering may be set for a pattern duration shorter (glitch capture) or longer, than a specified period. An "extended capture time" function even allows the final data capture to be postponed by an accurate time after trigger detection.

During continuous capture the screen may be updated at regular intervals or when a trigger condition is detected.

The large memory buffer allows much longer capture periods at higher sampling rates.

For slow changing signals the TE7100 may be used as a digital logger.



SPECIFICATIONS

TEAM SOLUTIONS, INC

You can start meaningful measurements almost immediately after installing the software for the first time. The comprehensive software provides a friendly and easily manageable environment for the wide spectrum of features. Setting a trigger condition, capturing and navigating through the data, is a pleasure. The data may be viewed, saved and printed in various useful formats.

If the Unit Under Test (UUT) is controlled by a PC, you do not require a second PC to run the logic analyzer software. Simply run your TE7100 and UUT software in different windows.

The TE7100 plugs in to the PC parallel port and is ideal for carrying around and moving from PC to PC.

Established in 1999, TEAM's mission is to provide its customers with state-of-the-art, PC-Based, Test & Measurement instrumentation. TEAM's products are offered in ISA, PCI, External (Parallel/USB driven), CompactPCI and PXI configuration.

Host system requirements	486 PC or compatible, 4MB RAM, 3MB disk space, VGA monitor or compatible, mouse, Windows 95 / 98 (NT4/2000 soon) or compatible
Digital channels	16 Inputs, 0 to 7V, 100K Ω /8pf input impedance, 40MHz input bandwidth
Internal sampling	Rates: MHz: 200, 100, 50, 25, 10, 5, 2.5, 1. KHz: 500, 250, 100, 50, 25, 10, 5, 2.5, 1.25 Stability: 100ppm
Digital logger clock	Log rate: 5 sec. to 1 hour in 1 sec. steps. Logger timing is obtained from the PC
External clock input	Clock on rising / falling edge, 25MHz max. Can handle limited or unlimited number of clock pulses
Data buffer	130000 (Decimal) samples per channel. The Pre/Post trigger buffer sizes may be set in 1000 bytes steps
Threshold voltage	1V, 1.3V, 1.5V, 1.7V, 2.0V, 2.2V, 2.4V, 3%
Extended capture time	Delay after trigger. Max depending on the sampling rate: 200MHz: 10 Ms max... 1.25KHz: 1677 s max
Trigger conditions	
Pattern	"1", "0" and "don't care" conditions selectable on all channels
Edge	On any one channel. Rising edge, falling edge, or change-of-state
Edge/Pattern combinations	Pattern, edge up, edge down, edge up OR edge down (Change-of-state) Pattern OR/AND/Then edge up/down/change-of-state. Edge up/down/change-of-state OR/AND/THEN pattern Pattern < duration (glitch capture). Pattern > duration. Duration 20ns tolerance, 1ms max. Edge and pattern triggering may be combined for single captures or for conditionally continuous capture
Continuous	Unconditionally: Screen updates at regular intervals. Conditionally: Display updates on detection of a trigger condition
Mouse/Keyboard	A trigger may be forced. THEN-setting conditions may be forced separately
Display	
Channel names/order/visible	User specified
Zooming	Zoom in, out, previous, all, between cursors
Continuous data display	Screen updates conditionally or unconditionally
Cursors	X, Y, Z-cursors and T-trigger line. Jump to functions. Signal value at the X and Y intersects are shown
Time measurements	Between any two cursors or trigger line. Frequency/period indications
Edge snapping	Cursors snap to exact edges in the data. Indicated on screen
Pattern search	"1", "0", and "don't care" conditions specified, also repeated search
Display formats	Timing (graphical). State: Hex and binary. Cursor positions indicated
Set-up/Trigger indicators	Set-up status bar: The trigger LED on the unit is mirrored on the main screen. Beep on trigger: On/off
Multitasking environment	Run the TE7100 and PC based Unit Under Test software from the same PC
Printed output	Timings print bitmap. Text: Hex and binary formats. Portrait or landscape
Files	Signal files: Contains captured data, date and time stamp, user information (e.g. heading, comments, etc.) channel name, cursor positions, software configuration, etc. When a signal file is reloaded, the software and data is set as when the file was saved Bitmap files: Bitmap of the main screen/bitmap similar to print presentation Text files: Hexadecimal format Configuration files: Software configuration. Auto-save option
LED indicators	Power, trigger
Port settings	278H (lpt2), 378H (lpt1), 3BCH (lpt-m)
Power requirements	Regulated (3%) +5V supply, 4.5W max - Active capturing, 1W max - not capturing. (The power supply is included in the standard package)
Dimensions and weight	150mm x 86mm x 26mm, 200g. Cables: Signal, Power: 1.5 - 1.8m
Operating temperature	10°C - 40°C (50°F - 105°F)

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27071 Cabot Road, Suite 124, Laguna Hills, CA 92653
Fax: 949.348.7776 • E-Mail: sales@team-solutions.com

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